

## Computer Science Engineering

Semester IV						
S. No	Course code	Course name	L	T	P	C
1	CS201T	<u>Automata Theory</u>	3	1	0	8
2	CS204T	<u>Artificial Intelligence</u>	3	0	0	6
3	CS302T	<u>Computer Architecture</u>	3	0	0	6
4	EE103T	<u>Digital Systems</u>	3	0	0	6
5	EE101L	<u>Digital Circuits Laboratory</u>	3	0	0	3
6	CS201L	<u>Artificial Intelligence Lab</u>	0	0	3	3
7	CS301L	<u>Computer Architecture Laboratory</u>	0	0	3	3
Total credits						35

## Computer Science Engineering

1	<b>Title of the course (L-T-P-C)</b>	<b>Automata Theory (3-1-0-8)</b>
2	<b>Pre-requisite courses(s)</b>	Exposure to Discrete Structures
3	<b>Course content</b>	Finite state machines (DFA/NFA/epsilon NFAs), regular expressions. Properties of regular languages. Myhill-Nerode Theorem. Non-regularity. Push down automata. Properties of context-free languages. Turing machines: Turing hypothesis, Turing computability, Nondeterministic, multi tape and other versions of Turing machines. Church's thesis, recursively enumerable sets and Turing computability. Universal Turing machines. Unsolvability, The halting problem, partial solvability, Turing enumerability, acceptability and decidability, unsolvable problems about Turing Machines. Post's correspondence problem.
4	<b>Texts/References</b>	<ol style="list-style-type: none"><li>1. Introduction to Automata Theory, Languages and Computation, by John. E. Hopcroft, Rajeev Motwani, J. D. Ullman, 3rd edition. Pearson. 2013.</li><li>2. Elements of the Theory of Computation, by H.R. Lewis and C. H. Papadimitrou, 2nd Edition. Prentice Hall Inc, 1998.</li></ol>

# Computer Science Engineering

1	<b>Title of the course (L-T-P-C)</b>	<b>Artificial Intelligence (3-0-0-6)</b>
2	<b>Pre-requisite courses(s)</b>	
3	<b>Course content</b>	<p>Search: Problem representation; State Space Search; A* Algorithm and its Properties; AO* search, Minimax and alpha- beta pruning, AI in games. Logic: Formal Systems; Notion of Proof, Decidability, Soundness, Consistency and Completeness; Predicate Calculus (PC), Resolution Refutation, Herbrand Interpretation, Prolog. Knowledge Representation: PC based Knowledge Representation, Intelligent Question Answering, Semantic Net, Frames, Script, Conceptual Dependency, Ontologies, Basics of Semantic Web. Learning: Learning from Examples, Decision Trees, Neural Nets, Hidden Markov Models, Reinforcement Learning, Learnability Theory. Uncertainty: Formal and Empirical approaches including Bayesian Theory, Fuzzy Logic, Non-monotonic Logic, Default Reasoning. Planning: Blocks World, STRIPS, Constraint Satisfaction, Basics of Probabilistic Planning.</p> <p>Advanced Topics: Introduction to topics like Computer ain.</p>
4	<b>Texts/References</b>	<p>Text: Stuart J. Russel, Peter Norvig, Artificial Intelligence: A Modern Approach (3rd ed.). Upper Saddle River: Prentice Hall, 2010. Other references: N.J. Nilsson, Principles of Artificial Intelligence, Morgan Kaufmann, 1985. Malik Ghallab, Dana Nau, Paolo Traverso, Automated Planning: Theory &amp; Practice, The Morgan Kaufmann Series in Artificial Intelligence, 2004. Christopher Bishop, Pattern Recognition and Machine Learning, Springer, 2006. Mark Stefik, Introduction to Knowledge Systems, Morgan Kaufmann, 1995. E. Rich and K.Knight, Artificial Intelligence, Tata McGraw Hill, 1992.</p>

# Computer Science Engineering

## Computer Science Engineering

1	<b>Title of the course</b> (L-T-P-C)	<b>Computer Architecture</b> <b>(3-0-0-6)</b>
2	<b>Pre-requisite courses(s)</b>	--
3	<b>Course content</b>	<p>The Language of Bits, Assembly Language, Logic Gates, Registers, and Memories, Processor Design, Principles of Pipelining, The Memory System, Multiprocessor Systems, I/O and Storage Devices.</p> <p>Each concept will be first taught on the basis of the fundamental driving principles. Following this, real world examples (e.g., ARM processors) will be used to emphasize the content.</p>
4	<b>Texts/References</b>	<ol style="list-style-type: none"><li>1. Computer Organization and Architecture, by Smruti Ranjan Sarangi, McGraw Higher Ed, 2017.</li><li>2. Computer Architecture a Quantitative Approach, Sixth edition, by David Patterson and John L. Hennesy, Morgan Kaufmann, 2017.</li></ol>

## Computer Science Engineering

# Computer Science Engineering

1	<b>Title of the course (L-T-P-C)</b>	<b>Digital Systems (2-1-0-6)</b>
2	<b>Pre-requisite courses(s)</b>	None
3	<b>Course content</b>	<ul style="list-style-type: none"> <li>• Introduction to Digital Systems</li> <li>• Number systems and Logic: Number Systems, Different Codes, Boolean logic, basic gates, truth tables</li> <li>• Introduction to Logic families: TTL, CMOS etc.</li> <li>• Boolean Algebra: Laws of Boolean Algebra, logic minimization using K maps</li> <li>• Combinational Logic Circuits: Adders, Subtractors, Multipliers, MSI components like Comparators, Decoders, Encoders, MUXs, DEMUXs</li> <li>• Sequential circuits: Latches, Flipflops, Analysis of clocked sequential circuits, Registers and Counters (Synchronous and Asynchronous), State Machines</li> <li>• Introduction to Hardware Description Languages</li> <li>• Array based logic elements: Memory, PLA, PLD, FPGA</li> <li>• Special Topics: Asynchronous State machines, Testing and Verification of Digital Systems</li> </ul>
4	<b>Texts/References</b>	<ol style="list-style-type: none"> <li>1. J. F. Wakerly: Digital Design, Principles and Practices, 4th Edition, Pearson Education, 2005</li> <li>2. M. Moris Mano; Digital Design, 4th Edition, Pearson, 2009</li> <li>3. Ronald J. Tocci; Digital System, Principles and Applications, 10th Edition, Pearson, 2009</li> <li>4. H. Taub and D. Schilling; Digital Integrated Electronics, McGraw Hill, 1977 Charles H Roth; Digital Systems Design using VHDL, Thomson Learning, 1998.</li> </ol>

# Computer Science Engineering

# Computer Science Engineering

1	<b>Title of the course (L-T-P-C)</b>	<b>Digital Circuits Laboratory (0-0-3-3)</b>
2	<b>Pre-requisite courses(s)</b>	Digital Systems Theory (EE224)
3	<b>Course content</b>	<p>This purpose of this lab is to complement the Digital Systems Theory Course. The following is the tentative list of experiments for this lab:Experiments with discrete ICs.</p> <ol style="list-style-type: none"><li>1. Introduction of digital ICs</li><li>2. Realizing Boolean expressions</li><li>3. Adder/Subtractor</li><li>4. Shift registers.</li><li>5. Synchronous Counters</li><li>6. Asynchronous Counters + 7- segment display Finite State Machines (2weeks) Experiments with CPLD</li><li>7. Arithmetic and Logic Unit</li><li>8. LCD, Buzzer Interfacing Pipelining</li></ol>
4	<b>Texts/References</b>	<ol style="list-style-type: none"><li>1. M. Moris Mano; Digital Design, 5th Edition, Pearson, 2009.</li><li>2. J.F.Wakerly: Digital Design, Principles and Practices,4th Edition, Pearson Education, 2009.</li><li>3. Ronald J. Tocci; Digital System, Principles and Applications, 10th Edition, Pearson, 2009</li></ol>

## Computer Science Engineering

1	<b>Title of the course (L-T-P-C)</b>	<b>Artificial Intelligence Lab (0-0-3-3)</b>
2	<b>Pre-requisite courses(s)</b>	
3	<b>Course content</b>	The lab will closely follow and aim to elucidate the lessons covered in the theory course CS344. Implementation and study of A*, Usage of Prolog Inferencing, Expert System Shells, Neural Net Platforms, Prediction and Sequence Labeling using HMMs, Simulation of Robot Navigation and such exercises are strongly recommended.
4	<b>Texts/References</b>	Stuart J. Russel, Peter Norvig, Artificial Intelligence: A Modern Approach (3rd ed.). Upper Saddle River: Prentice Hall, 2010. Other references: N.J. Nilsson, Principles of Artificial Intelligence, Morgan Kaufmann, 1985. Malik Ghallab, Dana Nau, Paolo Traverso, Automated Planning: Theory & Practice, The Morgan Kaufmann Series in Artificial Intelligence, 2004. Christopher Bishop, Pattern Recognition and Machine Learning, Springer, 2006. Mark Stefik, Introduction to Knowledge Systems, Morgan Kaufmann, 1995. E. Rich and K.Knight, Artificial Intelligence, Tata McGraw Hill, 1992.

## Computer Science Engineering

1	<b>Title of the course</b> (L-T-P-C)	<b>Computer Architecture Laboratory</b> <b>(0-0-3-3)</b>
2	<b>Pre-requisite</b> <b>courses(s)</b>	--
3	<b>Course content</b>	The lab will closely follow the theory course. The idea is to have the students develop a software model of a simple processor, capturing both functionality and timing aspects. They will implement modules as the concepts are taught in class.
4	<b>Texts/References</b>	Nil

## Computer Science Engineering